

The requirement for election of species is duly noted.

Responsive thereto, applicant provisionally elects species A, i.e. Figure 2C.

The claims readable thereon are:

1,2,3,5,6,8,12-14,18,19,22,23,and 25-27.

Applicant further traverses the requirement of election of species on the ground that there are at least several independent claims readable on all of the species A-C designated by the Examiner ( Figs. 2C,2D, 3, respectively). These include claims 1,2,3,5,8,12,-14,26,and 27 and for these reasons it is submitted that since these claims are generic to the species identified by the Examiner, the requirement for election of species should be withdrawn.

Nevertheless, in accordance with the Patent Office Rules, Applicant has provisionally elected the species A and has recited the claims readable thereon.


In the addition to the above, amendments has been introduced to claims 11 and 14 to correct minor informalities.

Early examination on the merits and allowance of the claims of this application are earnestly solicited.

Respectfully submitted,

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37 CFR §1.121(b)(1)(iii) and (c)(1)(ii) SPECIFICATION -9 2002  
AND CLAIM AMENDMENTS- MARKED UP VERSION

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11. (Twice Amended) Apparatus for reducing distortion of a signal applied to an input of a circuit operating at high frequency and having a parasitic [capcitanace]

capacitance, comprising:

a detecting circuit for detecting a change in voltage of said input signal; and

a correction circuit for changing an impedance of a parallel termination circuit that is in parallel with said parasitic capacitance to reduce distortion of said input signal.

14. (Twice Amended) Apparatus for reducing distortion of an input signal applied to an input of a circuit operating at high frequency and having a parasitic capacitance at said input, [compromising] comprising:

a first circuit element for selectively providing current to said parasitic capacitance;

a second circuit element for selectively preventing discharge of said parasitic capacitance; and

a control circuit monitoring said input signal for respectively turning on said first circuit element and turning off said second circuit element when a positive going edge of said input signal is detected and for turning off said first circuit element and turning on said second circuit element when a negative going edge of said input signal is detected.